

SYSTEM AND METHOD FOR VALIDATING PROCESSOR
PERFORMANCE AND FUNCTIONALITY

ABSTRACT OF THE DISCLOSURE

A method and system for accurately validating performance and functionality of a processor in a timely manner is provided. First, a program is executed on a high level simulator of the processor. Next, a plurality of checkpoints are established. Then, state data at each of the checkpoints is saved. Finally, the program is run on a plurality of low level simulators of the processor in parallel, where each of the low level simulators is started at a corresponding checkpoint with corresponding state data associated with the corresponding checkpoint.